

Failed

Saved

(3324) memory and packag53 and board and (register with address\$3)

(1825) memory and packag53 and (circuit near3 board) and (register with ad

(24) (memory and packag53 and (circuit near3 board) and ((register with ad

(155) ((memory near2 module) and pack\$4 and (pcb (printed near3 circuit ne

(458) (memory and module and packag53) and TSOP

(3) ((memory and module and packag53) and TSOP) and gigabyte

(626) (memory and module and packag53) and gigabyte

(3683) (memory and module and packag53) and (wide width) and (height high)

(1) (((memory and module and packag53) and TSOP) and ((memory and module a

(51) (memory and module and packag53) and (bilateral\$2 with symmetr\$6)

(27) (memory and module and packag53) and (bilateral with symmetr\$6)

(1) ((memory and module and packag53) and gigabyte) and (((memory and modu

(76) ((memory and module and packag53) and TSOP) and ((memory and module a

(145) ((memory and module and packag53) and gigabyte) and ((memory and mod

(422) memory and packag53 and (circuit near3 board) and ((register with ad

(312) (memory and packag53 and (circuit near3 board) and ((register with a

(25562) memory and module and packag53

(357) (memory and module and packag53) and (first adj row) and (second adj

(2) (memory and module and packag53) and (lateral\$2 near2 half)

(2) (memory and module and packag53) and (lateral\$2 near2 half\$3)

(4101) memory and module and ((IC chip) with arrang\$5)

(894) (memory and module and ((IC chip) with arrang\$5)) and ((IC chip) wit

(0) ((memory and module and ((IC chip) with arrang\$5)) and ((IC chip) with

(17922) 711/\$.cc1s.

(4521) 711/\$.cc1s. and module

(1369) (711/\$.cc1s. and module) and row

SEARCH

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18	<input type="checkbox"/>	<input type="checkbox"/>	US 4739473 A	19880419	16	Computer memory apparatus	711/167	711/105		Ng, Alvan W. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
19	<input type="checkbox"/>	<input type="checkbox"/>	US 6222739 B1	20010424	21	High-density computer module with stacked parallel-plane	361/790	257/686; 257/723;		Bhakta, Jayesh R. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
20	<input type="checkbox"/>	<input type="checkbox"/>	US 20030075789 A1	20030424	32	Semiconductor storage device having memory chips in a	257/678	257/E23.039; 257/E23.042;		Kawamura, Masayasu et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
21	<input type="checkbox"/>	<input type="checkbox"/>	US 20010050856 A1	20011213	29	Semiconductor device, module including the semiconductor	365/1			Matsuzaki, Yasuro	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
22	<input type="checkbox"/>	<input type="checkbox"/>	US 6617695 B1	20030909	20	Semiconductor device and semiconductor module using	257/778	257/686; 257/723;		Kasatani, Yasushi	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
23	<input type="checkbox"/>	<input type="checkbox"/>	US 6594167 B1	20030715	27	Semiconductor integrated circuit having a structure	365/51	365/230.08; 365/63		Yamasaki, Kyoji et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
24	<input type="checkbox"/>	<input type="checkbox"/>	US 6169325 B1	20010102	29	Semiconductor device	257/685	257/686; 257/723;		Azuma, Shuichiro et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
25	<input type="checkbox"/>	<input type="checkbox"/>	US 6766433 B2	20040720	10	System having user programmable addressing	711/202	711/220; 714/702		Circello, Joseph C. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
26	<input type="checkbox"/>	<input type="checkbox"/>	US 6594712 B1	20030715	38	Infiniband channel adapter for performing direct DMA	710/22	709/212; 710/26;		Pettey, Christopher et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
27	<input type="checkbox"/>	<input type="checkbox"/>	US 6353867 B1	20020305	22	Virtual component on-chip interface	710/305			Oureshi, Amjad et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
28	<input type="checkbox"/>	<input type="checkbox"/>	US 6185644 B1	20010206	29	Memory system including a plurality of memory devices	710/301	365/189.05		Farmwald, Michael et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>